

Dec. 13. 2001 11:15AM

FRISHAUF & PARTNERS

No. 3984 P. 1/2
From: HOLTZ

Attorney Docket No. 00791/LH

**IN THE UNITED STATES PATENT
AND TRADEMARK OFFICE**

Applicant(s): Takeshi WAKABAYASHI

Serial No. : 09/704,156

Filed : November 1, 2000

For : SEMICONDUCTOR DEVICE AND
METHOD OF MANUFACTURING
THE SAME

Art Unit : 2811

Examiner : L. C. THAI

RESPONSE - 37 C.F.R. 1.111

Assistant Commissioner for Patents
Washington, D.C. 20231

S I R :

This is responsive to the Office Action mailed December 4,
2001, 1998, the term for response to which expires on January 4,
2002.

ELECTION

Applicant hereby elects Group II, claims 11-16, drawn to a
method of making a semiconductor device, for further prosecution
on the merits, without traverse.

It is respectfully requested that prosecution on the merits
now proceed with respect to elected claims 11-16.

CERTIFICATE OF FACSIMILE
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TOTAL PAGES: Two (2)
I hereby certify that this
paper is being facsimile
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Asst. Commissioner for
Patents Office
on the date noted below

Attorney: Leonard Holtz

Dated: December 13, 2001

In the event that this Paper
is late filed, and the
necessary petition for
extension of time is not filed
concurrently herewith, please
consider this as a Petition
for the requisite extension of
time, and to the extent not
tendered by check attached
hereto, authorization to
charge the extension fee to
Account No. 06-1378.

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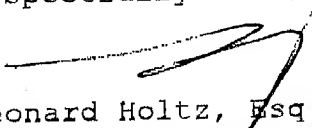
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No. 3984 P. 2/2

From: HOLTZ

If the Examiner has any comments, questions, objections or recommendations, the Examiner is invited to telephone the undersigned at the telephone number given below for prompt action.

Respectfully submitted,


Leonard Holtz, Esq.
Reg. No. 22,974

Dated: December 13, 2001

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